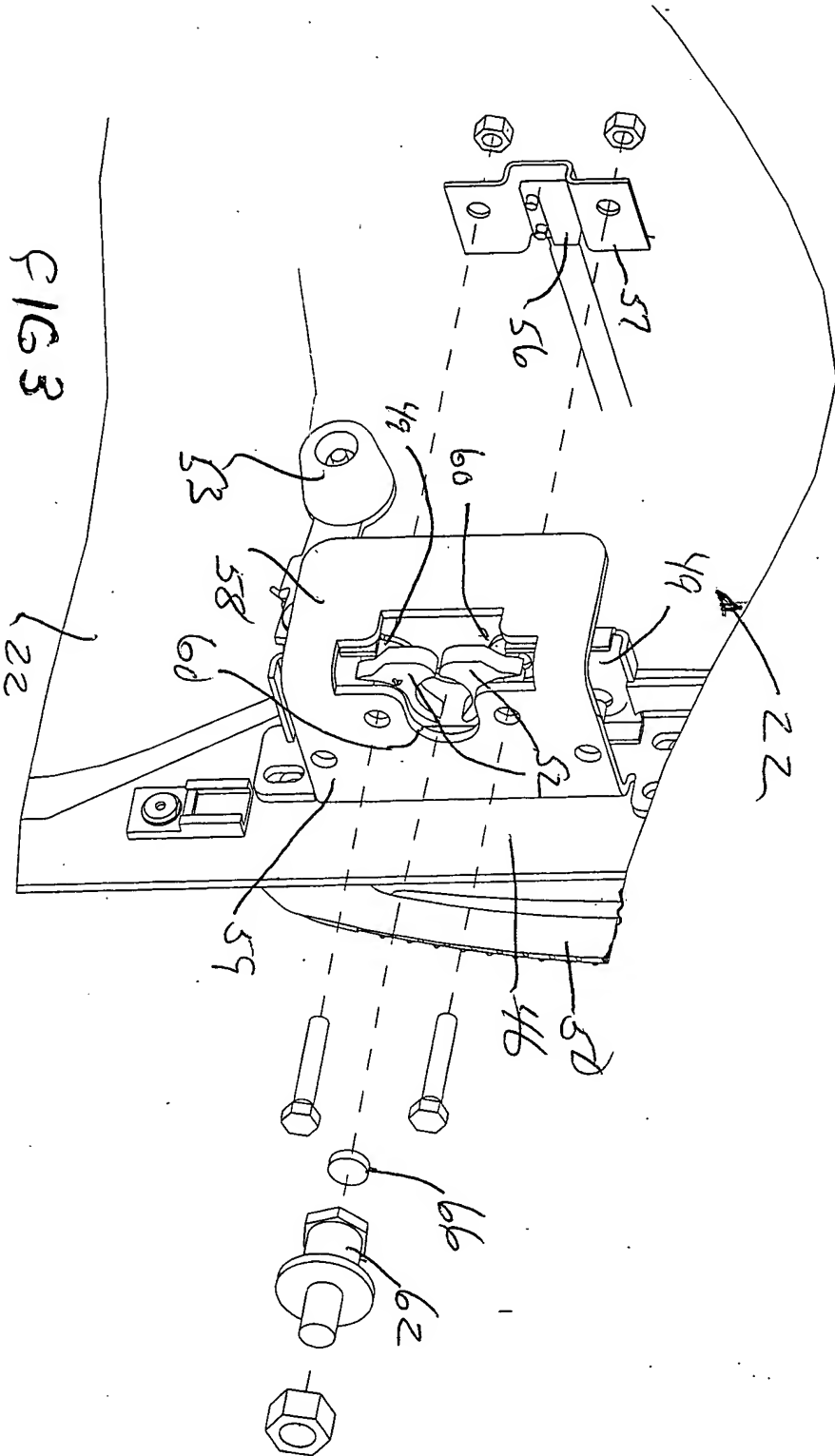
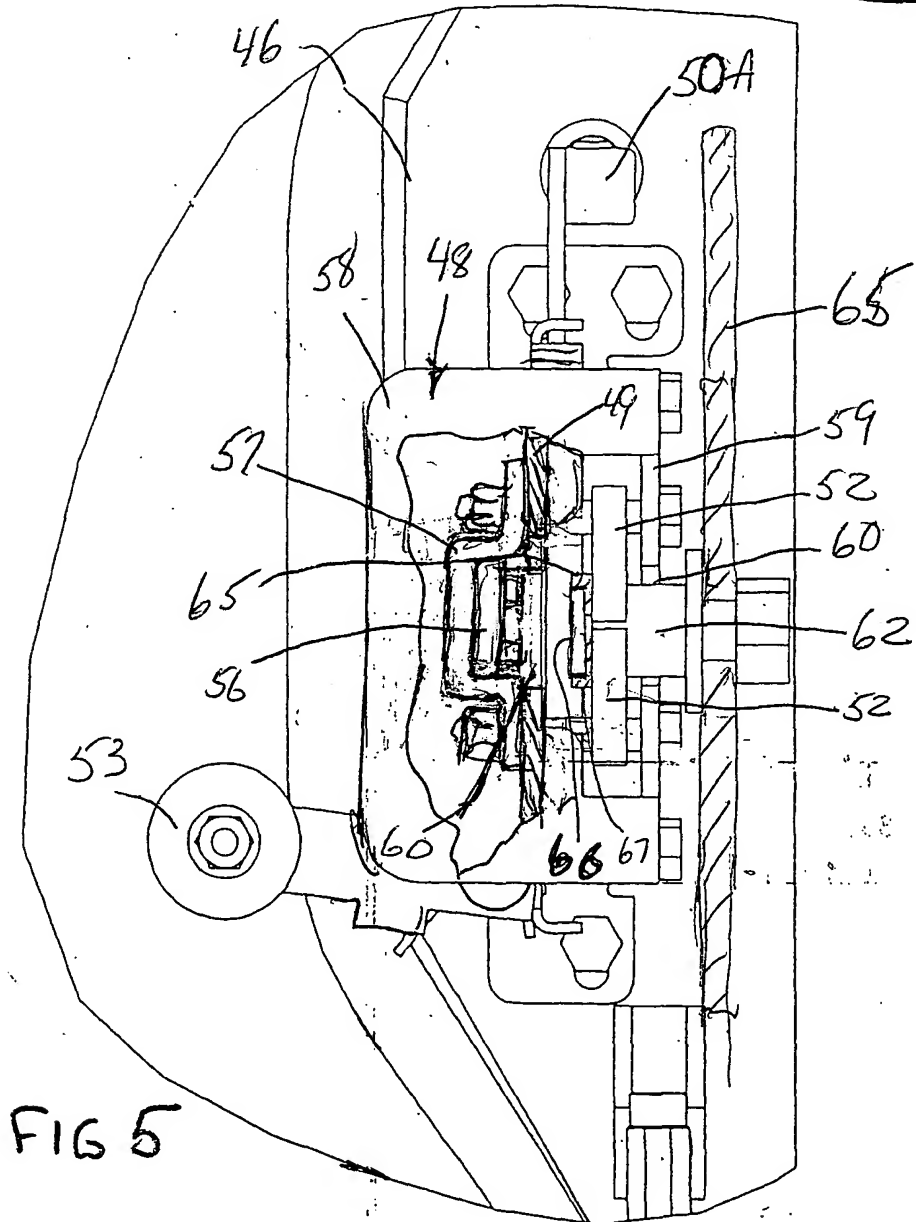
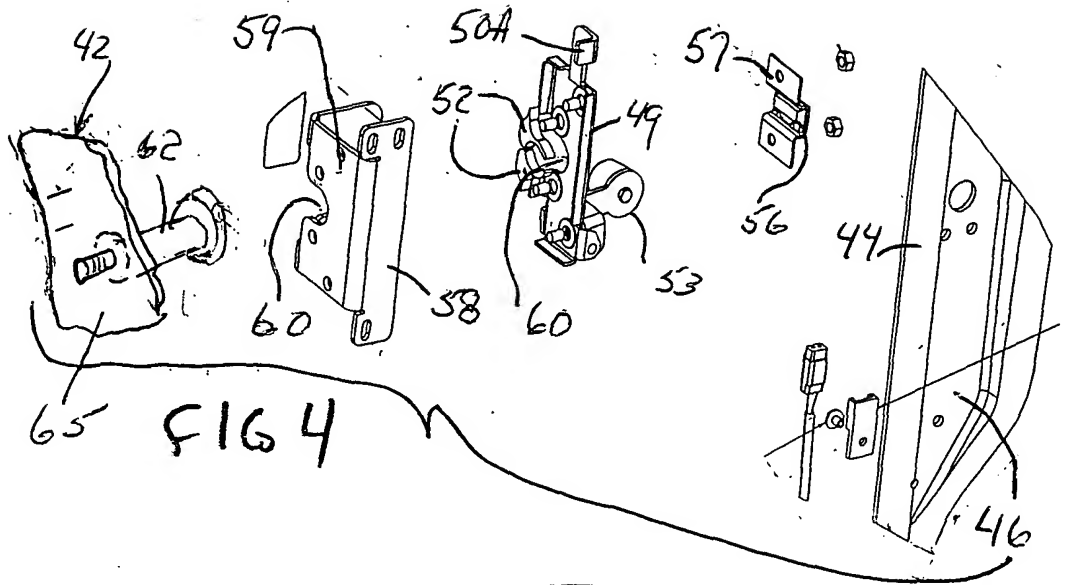


FIG 2

INTERLOCK
SYSTEM
CONTROLLER

32





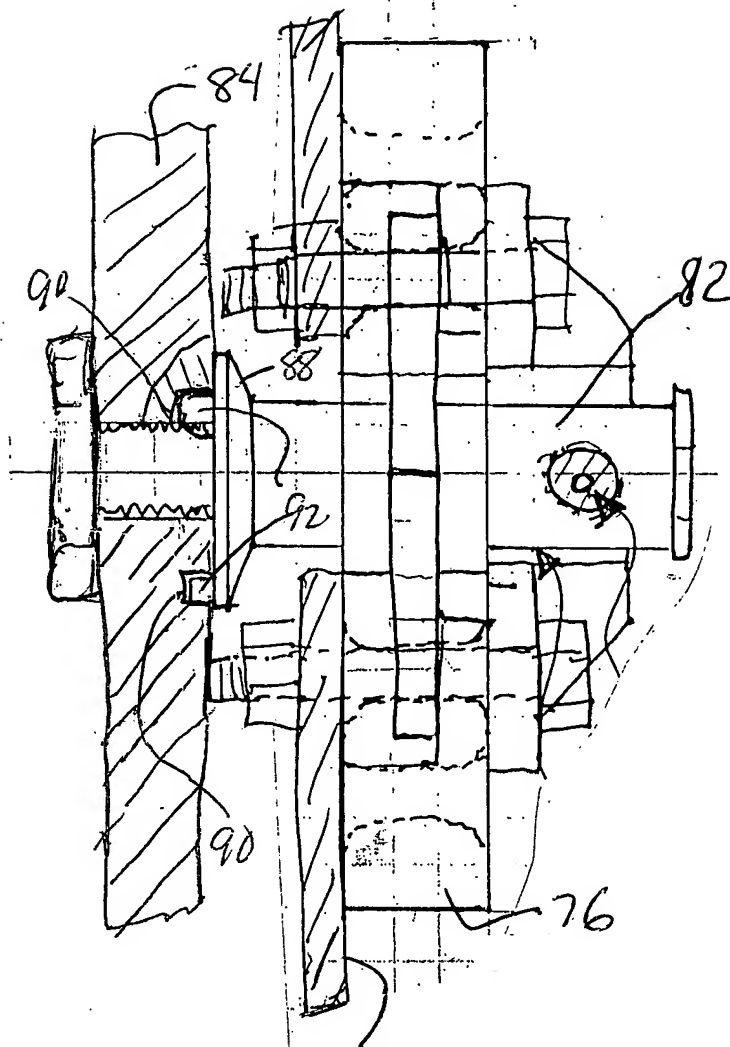


FIG 6

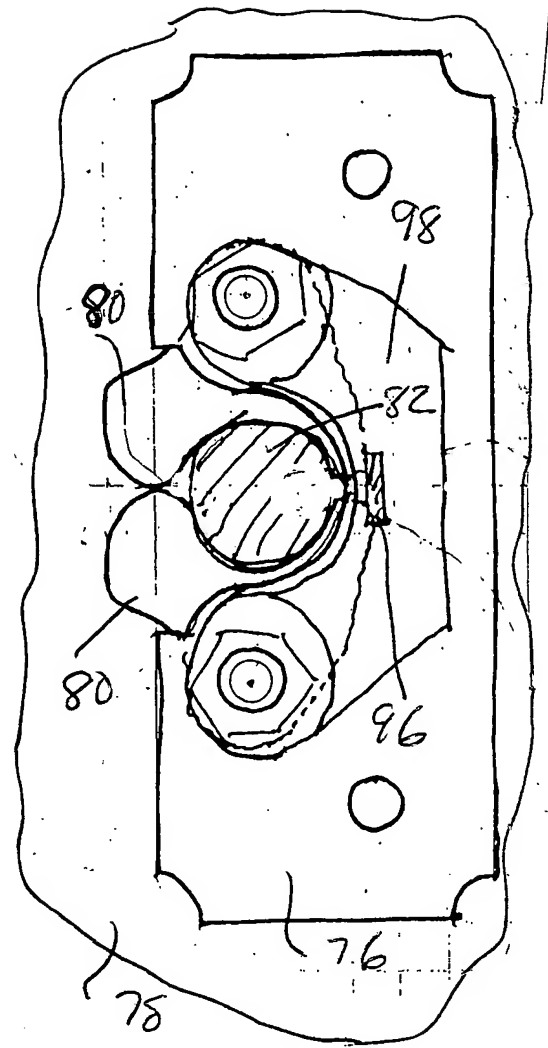


FIG 7

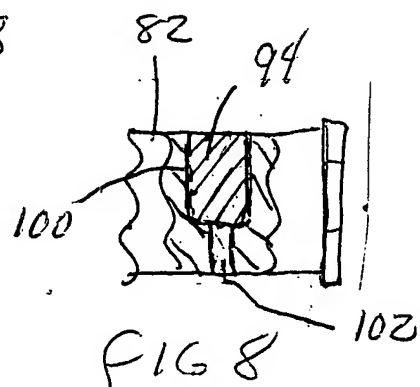


FIG 8